

IP-QFLK10(20)F34(B43)C(I)BD

SFP BIDI 1.25Gb/s TX1310nmFP/RX1490nm (TX1490nmDFB/RX1310nm) 10(20)km DDM

PRODUCT FEATURES

- Up to 1.25Gb/s data links
- 1310nm FP laser transmitter and PIN/TIA
 Receiver for IP-QFLK10(20)F34CBD
- 1490nm DFB laser transmitter and PIN/TIA
 Receiver for IP-QFLK10(20)B43CBD
- Up to 10(20)km on 9/125µm SMF
- Hot-pluggable SFP footprint
- BIDILC/UPC type pluggable optical interface
- Low power dissipation
- Metal enclosure, for lower EMI
- RoHS compliant and lead-free
- Support Digital Diagnostic Monitor interface
- Single +3.3V power supply
- Compliant with SFF-8472
- Case operating temperature

Commercial: 0°C to +70°C

Industrial: -40°C to +85°C

APPLICATIONS

- Switch to Switch Interface
- Fast Ethernet
- Switched Backplane Applications
- Router/Server Interface
- Other Optical Links



Compliance

- SFP MSA
- SFF-8472
- IEEE802.3z
- RoHS



PRODUCT DESCRIPTION

IP-QFLK10(20)F34(B43)CBD Small Form Factor Pluggable (SFP) transceivers are compatible with the Small Form Factor Pluggable Multi-Sourcing Agreement (MSA). The transceiver consists of five sections: the LD driver, the limiting amplifier, the digital diagnostic monitor, the 1310nm FP laser (the 1490nm DFB laser) and the PIN/TIA. The module data link up to 20km in 9/125umSingle-mode fiber.

This transceiver meets the Small Form Pluggable (SFP) industry standard package utilizing an integral LC-Bi-directional optical interface connector. An enhanced Digital Diagnostic Monitoring Interface compliant with SFF-8472 has been incorporated into the transceiver. It allows real time access to the transceiver operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage by reading a built-in memory with I2C interface.

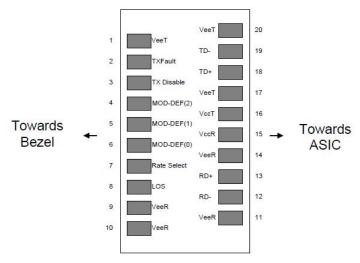
The optical output can be disabled by a LVTTL logic high-level input of Tx Disable, and the system also can disable the module via I2C. Tx Fault is provided to indicate that degradation of the laser. Loss of signal(LOS) output is provided to indicate the loss of an input optical signal of receiver or the link status with partner. The system can also get the LOS(or Link)/Disable/Fault information via I2C register access.

Ordering information

Package	Product part NO.	Data Rate(Mbps)	Media	Wavelength(nm)	Transmission Distance(km)	Temperate	ure Range(℃)
SFP	IP-QFLK10(20)F34(B43)CBD	1250	Single-mode fiber	1310T/1490R (1490T/1310R)	10(20)	0~70	Commercial
SFP	IP-QFLK10(20)F34(B43)IBD	1250	Single-mode fiber	1310T/1490R (1490T/1310R)	10(20)	-40~85	Industrial



I. Pin Diagram



Pinout of Connector Block on Host Board

II. Pin Descriptions

Pin	Symbol	Name/Description	Ref.
1	V _{EET}	Transmitter Ground (Common with Receiver Ground)	1
2	T _{FAULT}	Transmitter Fault. Open Drain. Logic "0" indicates normal operation.	2
3	T _{DIS}	Transmitter Disable. Laser output disabled on high or open.	3
4	MOD_DEF(2)	Module Definition 2. Data line for Serial ID.	4
5	MOD_DEF(1)	Module Definition 1. Clock line for Serial ID.	4
6	MOD_DEF(0)	Module Definition 0. Grounded within the module.	4
7	Rate Select	No connection required.	
8	LOS	Loss of Signal indication. Open Drain. Logic "0" indicates normal	5
9	V _{EER} Receiver Ground (Common with Transmitter Ground)		1
10	V _{EER}	Receiver Ground (Common with Transmitter Ground)	1
11	V _{EER}	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out(CML). AC Coupled	
13	RD+	Receiver Non-inverted DATA out(CML). AC Coupled	
14	V _{EER}	Receiver Ground (Common with Transmitter Ground)	1
15	V _{CCR}	Receiver Power Supply	
16	V _{CCT}	Transmitter Power Supply	
17	V _{EET}	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	V _{EET}	Transmitter Ground (Common with Receiver Ground)	1



Notes:

- 1. Circuit ground is internally isolated from chassis ground.
- 2. TX Fault is an open drain output, which should be pulled up with $4.7K 10K\Omega$ resistor on the host board. Pull up voltage between 2.0V to VccT/R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.When sensing an improper power level in the laser driver, the SFP sets this signal high and turns off the laser. TX-FAULT can be reset with the TX-DISABLE line. The signal is in LVTTL level.
- 3. TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with $4.7K 10K\Omega$ resistor. Its states are: Low (0 0.8V): Transmitter on; (>0.8, < 2.0V): Undefined; High (2.0V to VccT/R+0.3V): Transmitter Disabled; Open: Transmitter Disabled. The TX-DISABLE signal is high (LVTTL logic "1") to turn off the laser output. The laser will turn on when TX-DISABLE is low (LVTTL logic "0").
- 4. Should be pulled up with 4.7K 10KΩon host board to a voltage between 2.0V to VccT/R+0.3V. MOD_DEF(0) pulls line low to indicate module is plugged in.
- 5. LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with $4.7K 10K\Omega$ resistor. Pull up voltage between 2.0V to VccT/R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

The RX-LOS is high (LVTTL logic "1") when there is no incoming light from the companion transceiver. This signal is normally used by the system for the diagnostic purpose. The signal is operated in LVTTL level.

III. Absolute Maximum Ratings

Parameter	Symbol	Min.	Тур.	Max.	Unit	Ref.
Storage Temperature	TS	-40		85	^o	
Storage Ambient Relative Humidity	HA	0		85	%	
Power Supply Voltage	VCC	-0.5		4	V	
Signal Input Voltage		-0.3		Vcc+0.3	V	
Receiver Damage Threshold		+3			dBm	
Lead Soldering Temperature/Time	TSOLD			260/10	°C/sec	Note (1)
Lead Soldering Temperature/Time	TSOLD			360/10	°C/sec	Note (2)

Note (1): Suitable for wave soldering.

Note (2): Only for soldering by iron.



IV. Recommended Operating Conditions

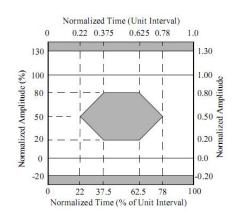
Parameter	Symbol	Min.	Тур.	Max.	Unit	Ref.
Case Operating	Tcase	0		70	°C	IP-QFLK10(20)F34(B43)CBD
Temperature		-40		85	30	IP-QFLK10(20)F34(B43)IBD
Ambient Humidity	HA	5		70	%	Non-condensing
Power Supply Voltage	VCC	3.13	3.3	3.47	V	
Power Supply Current	ICC			280	mA	
Data Rate			1250/1250		Mbps	TX Rate/RX Rate
Transmission Distance				20	km	
Coupled Fiber		Si	ngle mode fib	9/125um G.652		

V. Specification of Transmitter

Parameter	Symbol	Min.	Тур.	Max.	Unit	Ref.
Average Output Power	P _{OUT}	-9		-3	dBm	IP-QFLK10(20)F34(B43)C(I)BD
Extinction Ratio	ER	9			dB	
Contar Wayalangth	λ.	1260	1310	1360	nm	IP-QFLK10(20)F34C(I)BD
Center Wavelength	λς	1470	1490	1510	nm	IP-QFLK10(20)B43C(I)BD
Spectrum Width (RMS)	a			3.5	nm	FP Laser
Spectrum Width (KWS)	σ			3.5	11111	(TX:1310nm)
Side Mode Suppression Ratio	SMSR	30			dB	DFB Laser
Spectrum Bandwidth(-20dB)	σ			1	nm	(TX:1490nm)
Transmitter OFF Output Power	P _{Off}			-45	dBm	
Jitter p-p	t₃			0.1	IJ	Note (1)
Output Eye Mask	Complia	ant with	IEEE802.3 z			Note (2)
	(class 1 laser safety)					Note (2)

Note (1): Measure at 2^7-1 NRZ PRBS pattern.

Note (2): Transmitter eye mask definition.





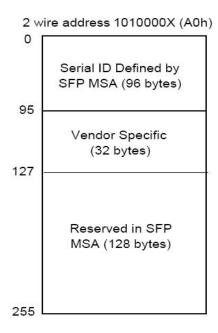
VI. Specification of Receiver

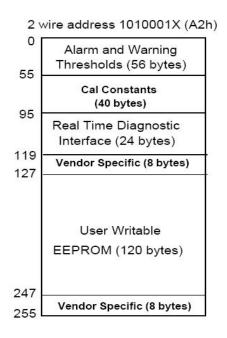
Parameter	Symbol	Min.	Тур.	Max.	Unit	Ref.
Input Optical Wayslandth	1	1440	1490	1540	nm	IP-QFLK10(20)F34C(I)BD
Input Optical Wavelength	λ _{IN}	1290	1310	1330	nm	IP-QFLK10(20)B43C(I)BD
Receiver Sensitivity	PIN			-23	dBm	Note (1)
Input Saturation Power	PSAT	-3			dBm	
(Overload)						
Loss of Signal Assert	PA	-45			dBm	
Loss of Signal De-assert	P _D			-23.5	dBm	Note (2)
LOS Hysteresis	P _D -P _A	0.5		6	dB	

Note (1): Measured with Light source 1490nm(1310nm), ER=9dB; BER =<10^-12 @PRBS=2^7-1 NRZ

Note (2): When LOS De-asserted, the RX data+/- output is signal output.

VII. Digital Diagnostic Memory Map





VIII.Digital Diagnostic Monitoring Information

Parameter	Unit	Accuracy
Case Temperature	$^{\circ}$	±3
Supply Voltage	V	±3%
Tx Bias Current	mA	±10%
Tx Optical Power	dB	±3



Rx Optical Power	dB	±3

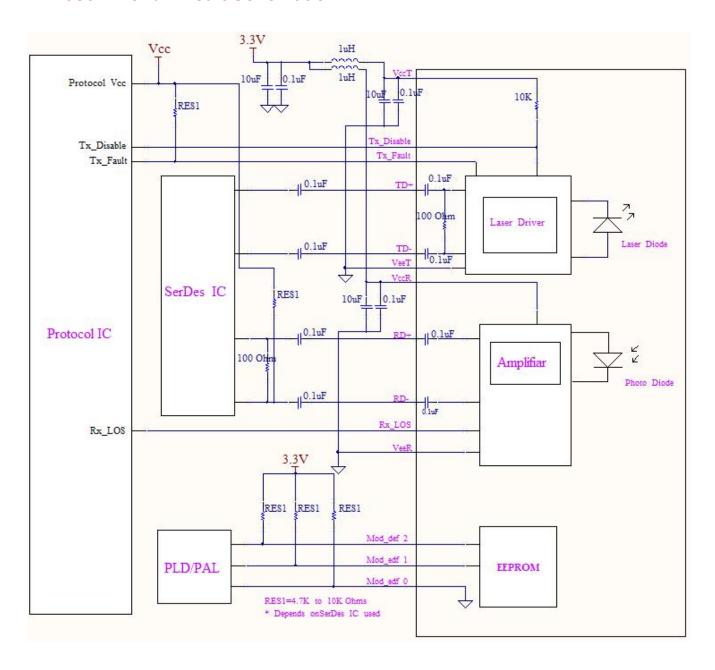
IX. Electrical Interface Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Unit	Ref.
Transmitter						
Total Supply Current	ICC			Α	mA	Note (1)
Transmitter Disable Input-High	VDISH	2		Vcc+0.3	٧	
Transmitter Disable Input-Low	VDISL	0		0.8	V	LVTTL
Transmitter Fault Input-High	VTxFH	2		Vcc+0.3	V	
Transmitter Fault Input-Low	VTxFL	0		0.8	V	
Receiver	Receiver					
Total Supply Current	ICC			В	mA	Note (1)
LOS Output Voltage-High	VLOSH	2		Vcc+0.3	٧	LVTTL
LOS Output Voltage-Low	VLOSL	0		0.8	V	LVIIL

Note (1): A (TX)+ B (RX) = 280mA (Not include termination circuit)

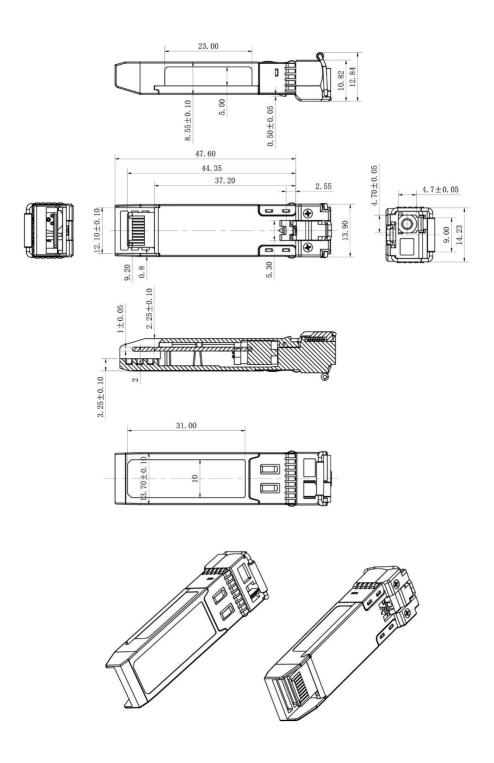


X. Recommend Circuit Schematic





XI. Mechanical Specifications(Unit: mm)



IP-QFLK10(20)F34(B43)C(I)BD



XII. Regulatory Compliance

Feature	Reference	Performance
EMC	EN61000-3	Compatible with standards
Electrostatic Discharge (ESD)	IEC/EN 61000-4-2	Compatible with standards
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN 55022 Class B (CISPR 22A)	Compatible with standards
Laser Eye Safety	FDA 21CFR 1040.10, 1040.11 IEC/EN 60825-1 ,EC/EN 60825-2	Class 1 laser product
Component Recognition	IEC/EN 60950 ,L 60950	Compatible with standards
ROHS	2002/95/EC	Compatible with standards

XIII.Contact Information

Wuhan Inphilight Technology Company Limited

NO.4 Building, Bonded Optoelectronics Industrial Park, Wuhan East Lake Comprehensive

Bonded Area, NO.777-30 Optics Valley 3rd Road, East Lake Hi-Technology Development

Zone, Wuhan, Hubei, China

Email: sales@inphilight.com

Revision History

Version No.	Date	Description
1.0	June 24, 2019	Preliminary datasheet
1.1	Aug 19,2021	Added industrial parameters
1.2	Sept.16,2021	According to the new naming rules,update PN.
1.3	Jun.30, 2024	Update contact information.